

UNIVERSITY OF KERALA
TIME TABLE FOR FIFTH SEMESTER BTECH DEGREE PRACTICAL EXAMINATION FOR
ELECTRONICS & COMMUNICATION ENGINEERING, JANUARY 2025- 2020 SCHEME

UNIVERSITY COLLEGE OF ENGINEERING KARIAVATTOM
REGULAR (2022 ADMISSION)/SUPPLEMENTARY (2020 & 2021 ADMISSION)

Course	Subject	Date	Centre
Electronics & Communication Engineering (412)	ECL 333 - Digital Signal Processing Lab	17.02.2025 FN & 18.02.2025 FN	University College of Engineering, Kariavattom
	ECL 331 - Analog Integrated Circuits and Simulation Lab	24.02.2025 FN & 25.02.2025 FN	

Thiruvananthapuram
Dated 07.02.2025

Sd/-
Controller of Examinations